

**WEST**[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

| Term  | Documents |
|---|-----------|
| LATCHING.USPT.  | 72641     |
| LATCHINGS.USPT.   | 54        |
| DISPLAY.USPT.   | 359160    |
| DISPLAYS.USPT.  | 150080    |
| DATA.USPT.  | 746878    |
| DATUM.USPT.   | 13732     |
| CLOCK.USPT.   | 197060    |
| CLOCKS.USPT.  | 38261     |
| SIGNAL.USPT.  | 838675    |
| SIGNALS.USPT.   | 658947    |
| (2 AND ((LATCHING ADJ DISPLAY ADJ DATA) SAME (CLOCK ADJ SIGNAL))).USPT. | 6         |
| (L2 AND LATCHING ADJ DISPLAY ADJ DATA SAME CLOCK ADJ SIGNAL).USPT.      | 6         |

**Database:**

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**Search:**

L4

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**Set Name Query**

side by side

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result set

*DB=USPT; PLUR=YES; OP=ADJ*

|           |  |       |           |
|-----------|--|-------|-----------|
| <u>L4</u> | L2 and latching adj display adj data same clock adj signal | 6     | <u>L4</u> |
| <u>L3</u> | L2 and latching adj display adj data                       | 28    | <u>L3</u> |
| <u>L2</u> | L1 and clock adj signal                                    | 5790  | <u>L2</u> |
| <u>L1</u> | (lcd or liquid adj crystal adj display)                    | 63871 | <u>L1</u> |

END OF SEARCH HISTORY

|   | Type | L # | Hits  | Search Text                            | DBs       | Time Stamp           | Comments |
|---|------|-----|-------|--|-----------|----------------------|----------|
| 1 | BRS  | L1  | 44858 | liquid adj crystal<br>adj display      | USPA<br>T | 2003/02/2<br>7 08:08 |          |
| 2 | BRS  | L3  | 2     | l2 and multiple adj<br>lines           | USPA<br>T | 2003/02/2<br>7 08:08 |          |
| 3 | BRS  | L2  | 524   | l1 and gate adj<br>driver\$1           | USPA<br>T | 2003/02/2<br>7 08:34 |          |
| 4 | BRS  | L4  | 1662  | l1 and bus adj line\$1                 | USPA<br>T | 2003/02/2<br>7 08:36 |          |
| 5 | BRS  | L5  | 7     | l4 and divided and<br>plural adj lines | USPA<br>T | 2003/02/2<br>7 08:39 |          |
| 6 | BRS  | L6  | 47689 | l1 bus adj lines and<br>divided        | USPA<br>T | 2003/02/2<br>7 08:40 |          |
| 7 | BRS  | L7  | 443   | l1 and bus adj lines<br>and divided    | USPA<br>T | 2003/02/2<br>7 08:40 |          |
| 8 | BRS  | L8  | 30    | l7 and clock adj<br>lines and divided  | USPA<br>T | 2003/02/2<br>7 08:40 |          |

**WEST****Freeform Search**

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result set

*DB=USPT; PLUR=YES; OP=ADJ*

|           |   |       |           |
|-----------|---|-------|-----------|
| <u>L8</u> | L7 and divided adj bus adj line\$1      | 7     | <u>L8</u> |
| <u>L7</u> | bus adj line\$1                         | 14544 | <u>L7</u> |
| <u>L6</u> | L1 and divided adj clock adj line\$1    | 1     | <u>L6</u> |
| <u>L5</u> | L4 and divided adj clock adj line\$1    | 1     | <u>L5</u> |
| <u>L4</u> | l1 and clock adj line\$1                | 675   | <u>L4</u> |
| <u>L3</u> | L1 and divided adj bus adj lines        | 2     | <u>L3</u> |
| <u>L2</u> | L1 and divided adj bus lines            | 2     | <u>L2</u> |
| <u>L1</u> | (LCD or liquid adj crystal adj display) | 63871 | <u>L1</u> |

END OF SEARCH HISTORY

|    | Type | L # | Hits  | Search Text   | DBs       | Time Stamp           | Comments |
|----|------|-----|-------|---|-----------|----------------------|----------|
| 1  | BRS  | L1  | 44858 | liquid adj crystal<br>adj display   | USPA<br>T | 2003/02/2<br>6 12:47 |          |
| 2  | BRS  | L2  | 5115  | l1 and circuit adj<br>board\$1  | USPA<br>T | 2003/02/2<br>6 12:48 |          |
| 3  | BRS  | L3  | 440   | l2 and drive adj<br>circuit\$1  | USPA<br>T | 2003/02/2<br>6 12:48 |          |
| 4  | BRS  | L5  | 42    | l4 and connect\$2   | USPA<br>T | 2003/02/2<br>6 12:49 |          |
| 5  | BRS  | L4  | 42    | l3 and bus adj line\$1  | USPA<br>T | 2003/02/2<br>6 12:50 |          |
| 6  | BRS  | L6  | 68394 | printed adj circuit<br>adj board\$1                                       | USPA<br>T | 2003/02/2<br>6 13:03 |          |
| 7  | BRS  | L7  | 890   | l6 and bus adj lines  | USPA<br>T | 2003/02/2<br>6 13:04 |          |
| 8  | BRS  | L8  | 0     | l7 and bus adj lines<br>adj on adj printed<br>adj circuit adj<br>board\$1 | USPA<br>T | 2003/02/2<br>6 13:05 |          |
| 9  | BRS  | L9  | 89    | l7 and bus adj lines<br>same printed adj<br>circuit adj board\$1          | USPA<br>T | 2003/02/2<br>6 13:06 |          |
| 10 | BRS  | L10 | 24    | l9 and display\$1   | USPA<br>T | 2003/02/2<br>6 13:06 |          |

|   | Type | L # | Hits       | Search Text                    | DBs       | Time Stamp          | Comments |
|---|------|-----|------------|--------------------------------|-----------|---------------------|----------|
| 1 | IS&R | L1  | 0          | ("display").PN.                | USPA<br>T | 2003/02/26<br>10:40 |          |
| 2 | BRS  | L2  | 35916<br>0 | display                        | USPA<br>T | 2003/02/26<br>10:40 |          |
| 3 | BRS  | L3  | 24132      | 12 and circuit adj<br>board\$1 | USPA<br>T | 2003/02/26<br>10:41 |          |
| 4 | BRS  | L4  | 6177       | 13 and liquid adj<br>crystal   | USPA<br>T | 2003/02/26<br>10:42 |          |
| 5 | BRS  | L5  | 543        | 14 and drive adj<br>circuit\$1 | USPA<br>T | 2003/02/26<br>10:43 |          |
| 6 | BRS  | L6  | 21813      | 15 an display adj<br>control   | USPA<br>T | 2003/02/26<br>10:44 |          |
| 7 | BRS  | L7  | 457        | 14 and display adj<br>control  | USPA<br>T | 2003/02/26<br>10:45 |          |
| 8 | BRS  | L8  | 60         | 17 and drive adj<br>circuit\$1 | USPA<br>T | 2003/02/26<br>10:45 |          |
| 9 | BRS  | L9  | 4          | 18 and bus adj line\$1         | USPA<br>T | 2003/02/26<br>10:46 |          |